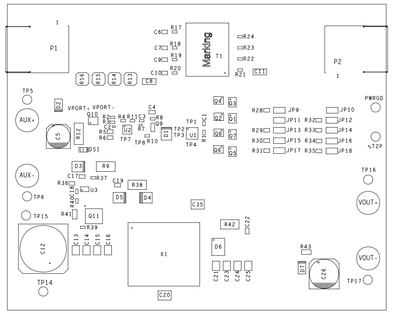
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	24NOV23	D.BOISER
B	ECR-119026	07FEB24	D.BOISER



ASSEMBLY NOTES:

1. BOARD ACCEPTABILITY PER ANALOG DEVICES, INC. SPECIFICATION TST00119 (LATEST REVISION).
2. REPAIRS PER IPC-7711/21(LATEST REVISION) ARE ALLOWED.
3. REPAIRS ARE NOT ALLOWED IN SOLDERMASK FREE AREAS ON EITHER SIDE OF THE BOARD.
4. INSTALL P/N: SPN02SYBN-RC ON JP9~JP10 (PIN 1 AND PIN 2)

PRIMARY SIDE

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES DECIMALS FRACTIONS ANGLES .XX +-.010 +-1/32 +- 2 .XXX +-.005 .XXXX +-.0050	APPROVAL	DATE	ANALOG DEVICES AHEAD OF WHAT'S POSSIBLE™ GLOBAL OPERATIONS & TECHNOLOGY 804 WOBURN STREET WILMINGTON, MA 01887
	TEMPLATE ENGINEER BILLY PHILLIPS	14APR21	
	HARDWARE SERVICES BOB MACDONALD	14APR21	
	HARDWARE SYSTEMS DAVE WILLIAMS	14APR21	
MATERIAL	TEST ENGINEER N/A	N/A	TITLE ASSEMBLY LT4275B and LT8306 25.5W POE CLASS 2
	COMPONENT ENGINEER ADGT LIBRARY	24NOV23	
	TEST PROCESS N/A	N/A	
	HARDWARE RELEASE C. LAPPAY	07FEB24	
FINISH	PCB DESIGNER C. LAPPAY	07FEB24	SIZE D / FSCM NO 24355 / DRAWING NUMBER 01-081007 / REV B
	ENGINEER D. BOISER	07FEB24	
	CHECKER N/A	N/A	
DO NOT SCALE DWG			SCALE 1/1 / SHEET 1 OF 1